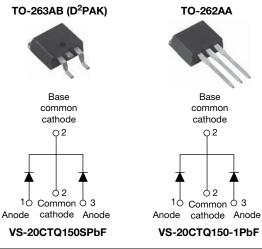


Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 10 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 10 A			
V _R	150 V			
V _F at I _F	0.66 V			
I _{RM} max.	5.0 mA at 125 °C			
T _J max.	175 °C			
E _{AS}	1.0 mJ			
Package	TO-263AB (D ² PAK), TO-262AA			
Diode variation	Common cathode			

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	20	A		
V _{RRM}		150	V		
I _{FSM}	t _p = 5 μs sine	1030	A		
V _F	10 A _{pk} , T _J = 125 °C (per leg)	0.66	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-20CTQ150SPbF VS-20CTQ150-1PbF	UNITS	
Maximum DC reverse voltage	V _R	150	V	
Maximum working peak reverse voltage	V _{RWM}	150	v	

ABSOLUTE MAXIMUM RATI	NGS				
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward per l				10	
current See fig. 5 per devi	ce I _{F(AV)}	50 % duty cycle at T_C = 154 °C, rectangular waveform		20	•
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	1030	A
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	180	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 2 \text{ mH}$		1.0	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim		1	А

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VS-20CTQ150SPbF, VS-20CTQ150-1PbF

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS
		10 A	T.I = 25 °C	0.80	0.88	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	20 A	1j=25 0	0.90	1.0	V
See fig. 1	VFM W	10 A	T _{.1} = 125 °C	0.63 0.66 V 0.73 0.77 3.0 25 μA		
		20 A	1j=125 0	0.73	0.77	
Maximum reverse leakage current per leg	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3.0	25	μA
See fig. 2	IRM \	T _J = 125 °C	VR - naleu VR	2.7	5.0	mA
Typical junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		-	280	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		-	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

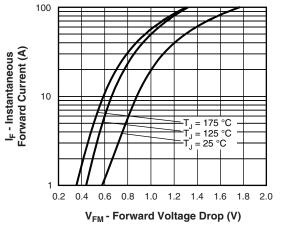
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

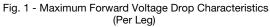
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C
Maximum thermal resistance,	Maximum thermal resistance, per leg		DC operation	2.0	
junction to case	per package	R _{thJC} DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (Only for TO-262)		
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf · in)
Marking davias			Case style D ² PAK	20CTC	150S
Marking device			Case style TO-262	20CTC	150-1

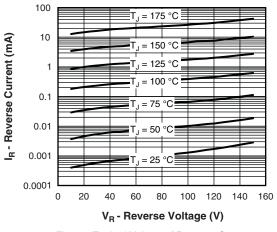
VS-20CTQ150SPbF, VS-20CTQ150-1PbF

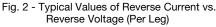


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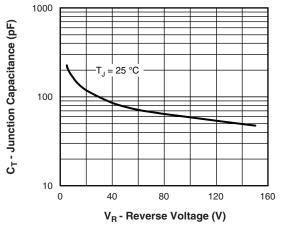


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

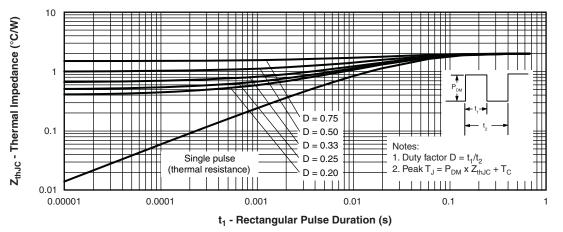
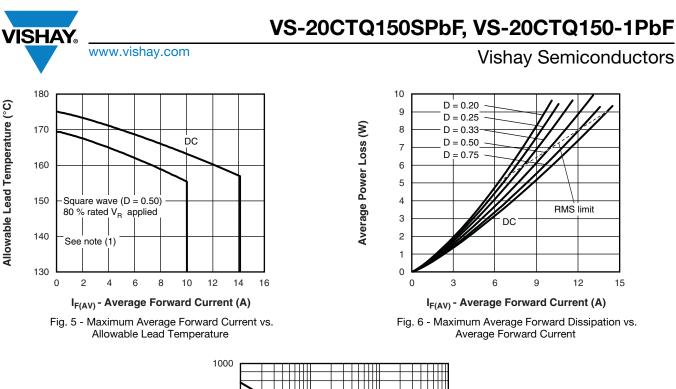
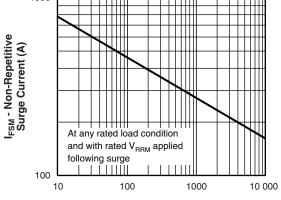


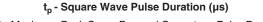
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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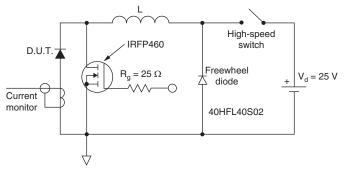


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \\ \end{array}$

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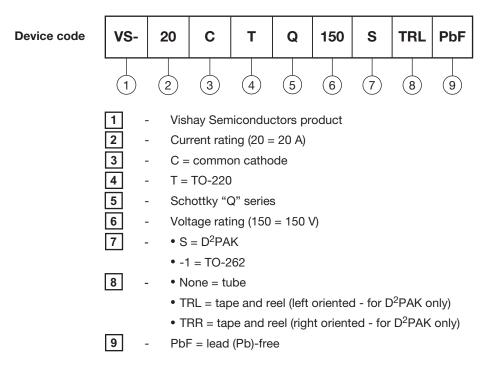


Vishay Semiconductors

ORDERING INFORMATION TABLE

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ISHA



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-20CTQ150SPbF	50	1000	Antistatic plastic tubes		
VS-20CTQ150STRLPbF	800	800	13" diameter reel		
VS-20CTQ150STRRPbF	800	800	13" diameter reel		
VS-20CTQ150-1PbF	50	1000	Antistatic plastic tubes		

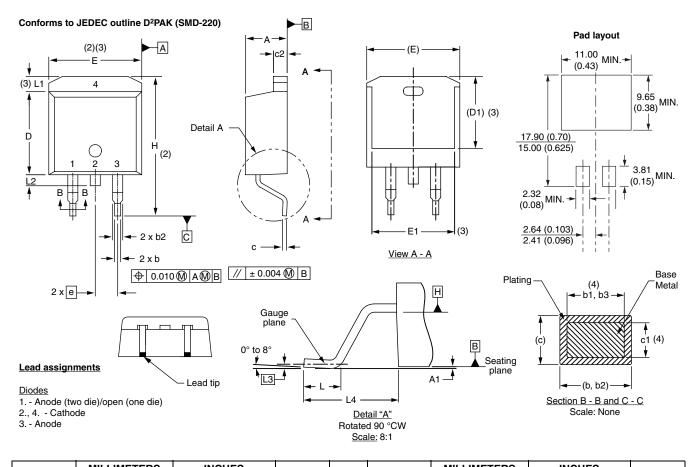
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032

Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches

SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Notes

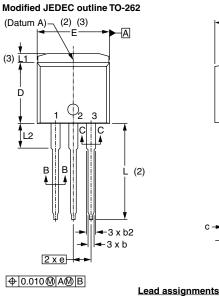
- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch

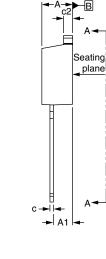
Vishay High Power Products

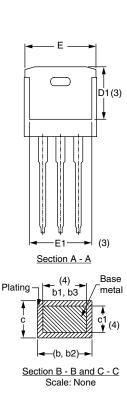
D²PAK, TO-262



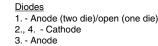
DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



SYMBOL -	MILLIM	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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